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Lam

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(54) **BALANCED AMPLIFIER**

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(*) Notice: Subject to any disclaimer, the term of this patent is extended or adjusted under 35 U.S.C. 154(b) by 114 days.

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See application file for complete search history.

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(57) **ABSTRACT**

A balanced amplifier is disclosed as comprising an input stage connected with a second stage, in which the input stage outputs signals to the second stage, and the first stage includes a first differential amplifier for receiving balanced input signals and is provided with two pairs of feedback loops, at least one of the pair of feedback loops for feedbacking part of signals outputted by the second stage to the first differential amplifier, and the second stage includes a second differential amplifier for amplifying signals received from the first stage and for maintaining the balanced signals.

20 Claims, 13 Drawing Sheets

